

JCS64 U.S. PRO
09/490748
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| ISSUE CLASSIFICATION | |
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| Class | Subclass |
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PATENT NUMBER:

U.S. UTILITY Patent Application

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| O.I.P.E. | PATENT DATE |
| RW SCANNED A.G. Dec 99 | |

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| APPLICATION NO. 09/490748 | CONT/PRIOR D | CLASS 372 | SUBCLASS 102 | ART UNIT 2874- 2878 2881 | EXAMINER <u>ZAHN</u> REED |
| APPLICANTS John D. Zahn, et al. | | | | | |
| TITLE Method of forming integrated circuits and associated methods | | | | | |

PTO-2040
12/89

ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

| TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
|---|--------------|-------------|------------|-----------------------------------|----------------------|
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | | | | NOTICE OF ALLOWANCE MAILED | |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____ | | | | ISSUE FEE | |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | | | | Amount Due | Date Paid |
| | | | | ISSUE BATCH NUMBER | |
| WARNING: | | | | | |

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